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FACSIMILE TRANSMITTAL

Date:

June 21, 2005

To:

Examiner John J. Tabone, Jr.

Organization:

U.S. Patent and Trademark Office

Fax Number:

(703) 872-9306

Phone Number:

(571) 272-3827

From:

Kelvin M. Vivian, Reg. No. 53,727

Re:

10/016,449 - Amendment Response

(IBM - 2280P)

This is page 1 of 13 pages.

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TRANSMITTAL FORM

In re the application Robert T. BAILIS, et al.

Confirmation No: 5286

Serial No: 10/016,449

Group Art Unit: 2133

Filed:	Dece	mber 10,	2001		Examiner: Tabone Jr., John J.					
For: Method and System for Use of a Field Programmable Gate Array (FPGA) Function within an Application Specific Integrated Circuit (ASIC) to Enable Creati										
ENCLOSURES (check all that apply)										
	Amendment/Reply				Assignment and Red Cover Sheet	cordation		After Allowance Communication to Group		
	After Final				Part B-Issue Fee Tra	ansmittal [Notic	Notice of Appeal		
	Information disclosure statement				Letter to Draftsman		Appe	Appeal Brief		
		Form 144	19		Drawings		Statu	Status Letter		
	(X) Copies of References				Petition		Postcard			
	Extension of Time Request *				Fee Address Indicat	ion Form	Othe ident	Other Enclosure(s) (please identify below):		
一	Express Abandonment				Terminal Disclaimer					
	Certified Copy of Priority Doc				Power of Attorney and Revocation of Prior Powers					
	Resp	onse to Inc	complete Appin		Change of Correspondence Address					
	Resp		ssing Parts Declaration by	c	*Extension of Term: Pursuant to 37 CFR 1.136, Applicant petitions the Commissioner to extend the time for response for xxxxx month(s), from to .					
CLAIMS										
	FC	R	Claims Rem After Amend		Highest # of Claims Previously Paid For	Extra Clain	18	RATE	FEE	
Total	Claims		13		20	0	\$	50.00	\$ 0.00	
			2		3	0		200.00	\$ 0.00	
Independent Claims 2								Total Fees	\$ 0.00	
					METHOD OF PAYM	ENT				
	Check no in the amount of \$ is enclosed for payment of fees.									
Charge any fees or credit any overpayment to Deposit Account No. <u>50-0563</u> (IBM Corporation)										
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT										
Atton	ney Na	me l	Kelvin M. Vivlan	Reg. I	o. 53,727					
Signature M. N(-:										
Date June 21, 2005										
CERTIFICATE OF FACSIMILE										
I hereby certify that this correspondence is being faxed to Examiner John J. Tabone, Jr. (703-872-9306) at the USPTO, on June 21, 2005										
Type or printed name Irena Nikolova										
Company of Prince Princ										

Attorney Docket: RP\$920010127U\$1/2280P

CERTIFICATE OF FACSIMILE

I hereby certify that this correspondence is being fixed to Examiner John J. Tabone, Jr. (703-872-9306) at the USPTO,

on June 21, 2005

Irena Nikolova

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In Re Application of:

Date: June 21, 2005

111N 9 1 200E

Robert T. BAILIS, et al.

Confirmation No: 5286

JUN 2 1 2005

Serial No: 10/016,449

Group Art Unit: 2133

Filed: December 10, 2001

Examiner: John J. Tabone, Jr.

For:

METHOD AND SYSTEM FOR USE OF A FIELD PROGRAMMABLE GATE

ARRAY (FPGA) FUNCTION WITHIN AN APPLICATION SPECIFIC

INTEGRÀTED CIRCUIT (ASIC) TO ENABLE CREATION OF A DEBUGGER

CLIENT WITHIN THE ASIC

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF MARCH 23, 2005

In response to the Office Action dated March 23, 2005, please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 6 of this paper.